

FIGURE 1

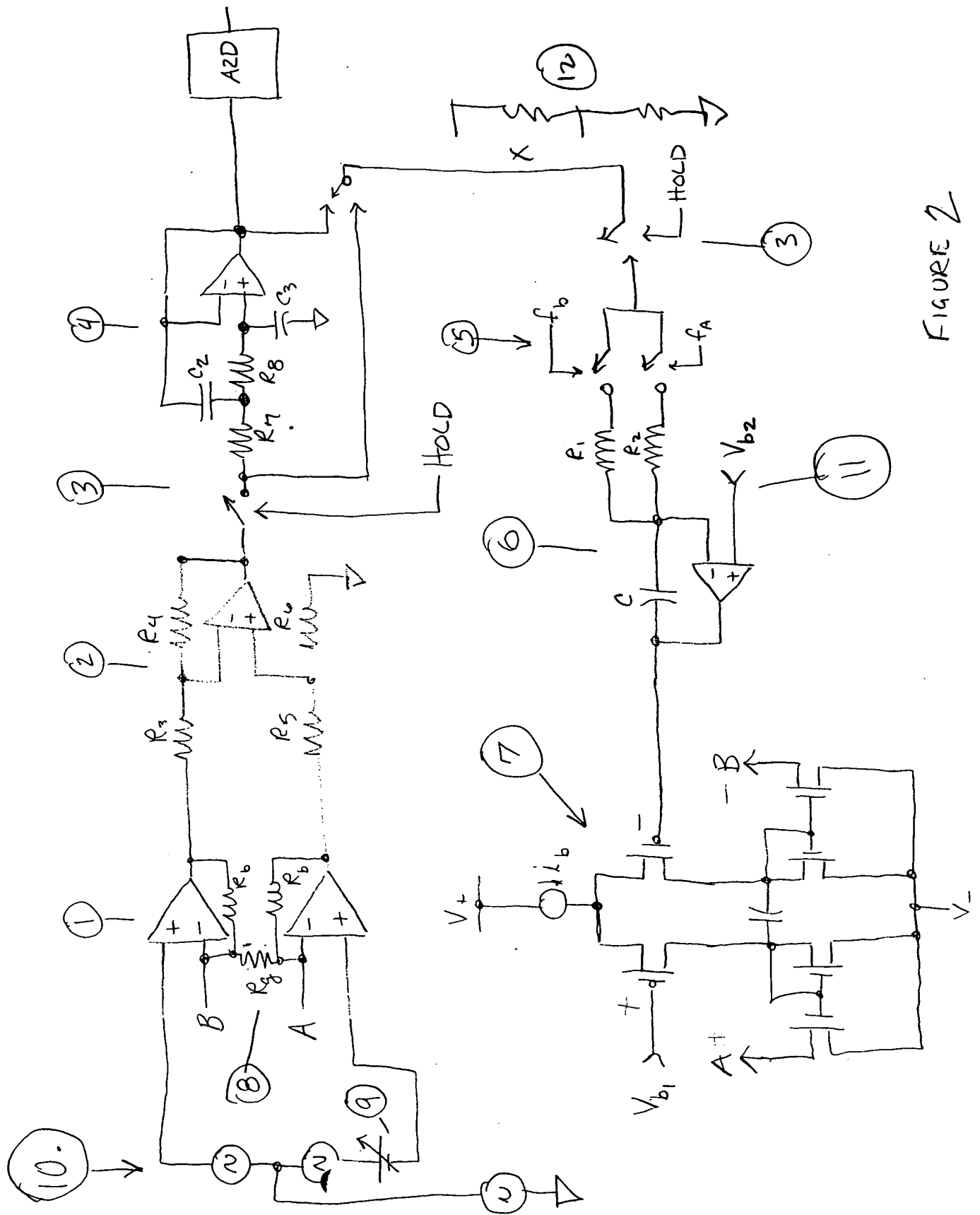


FIGURE 2

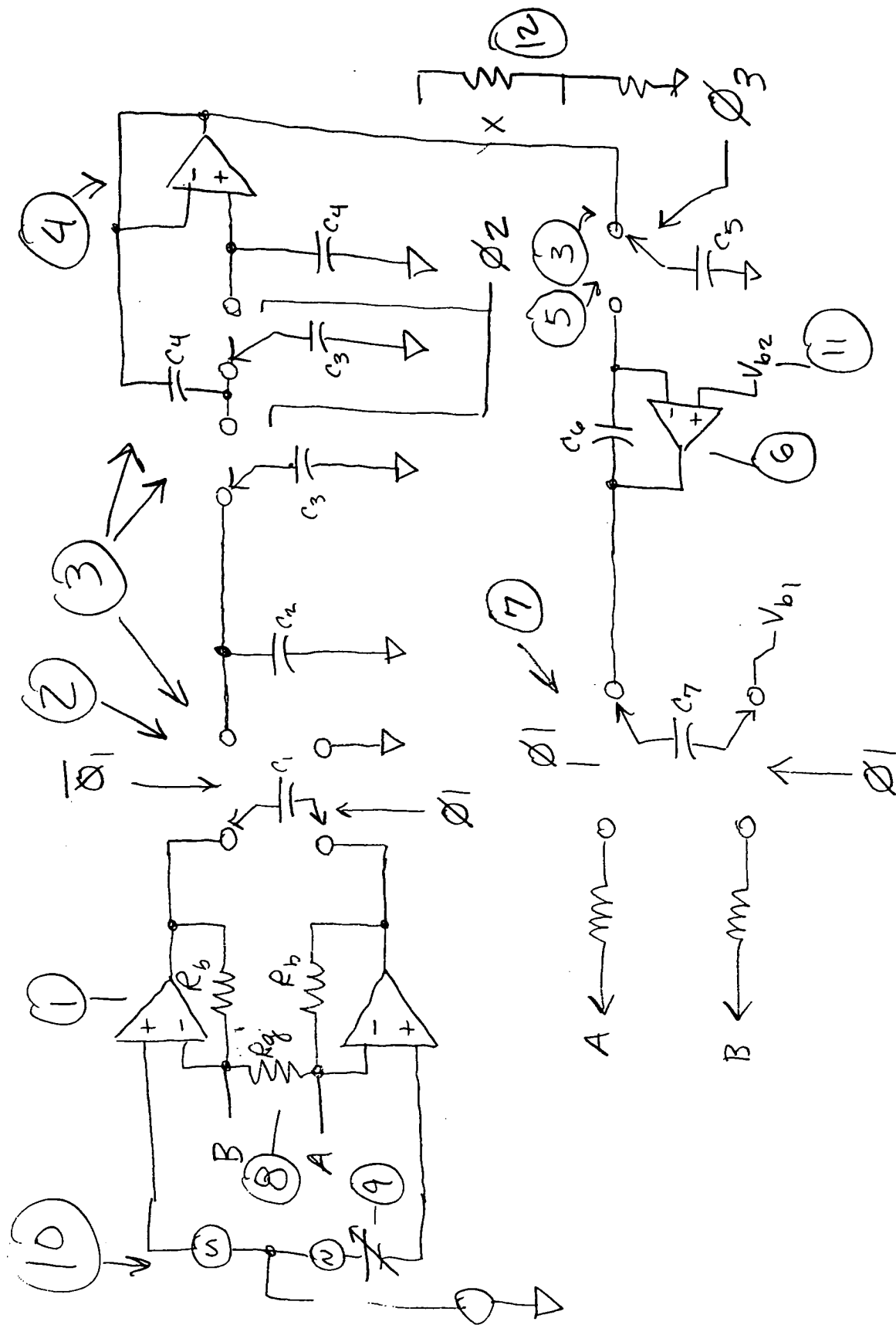
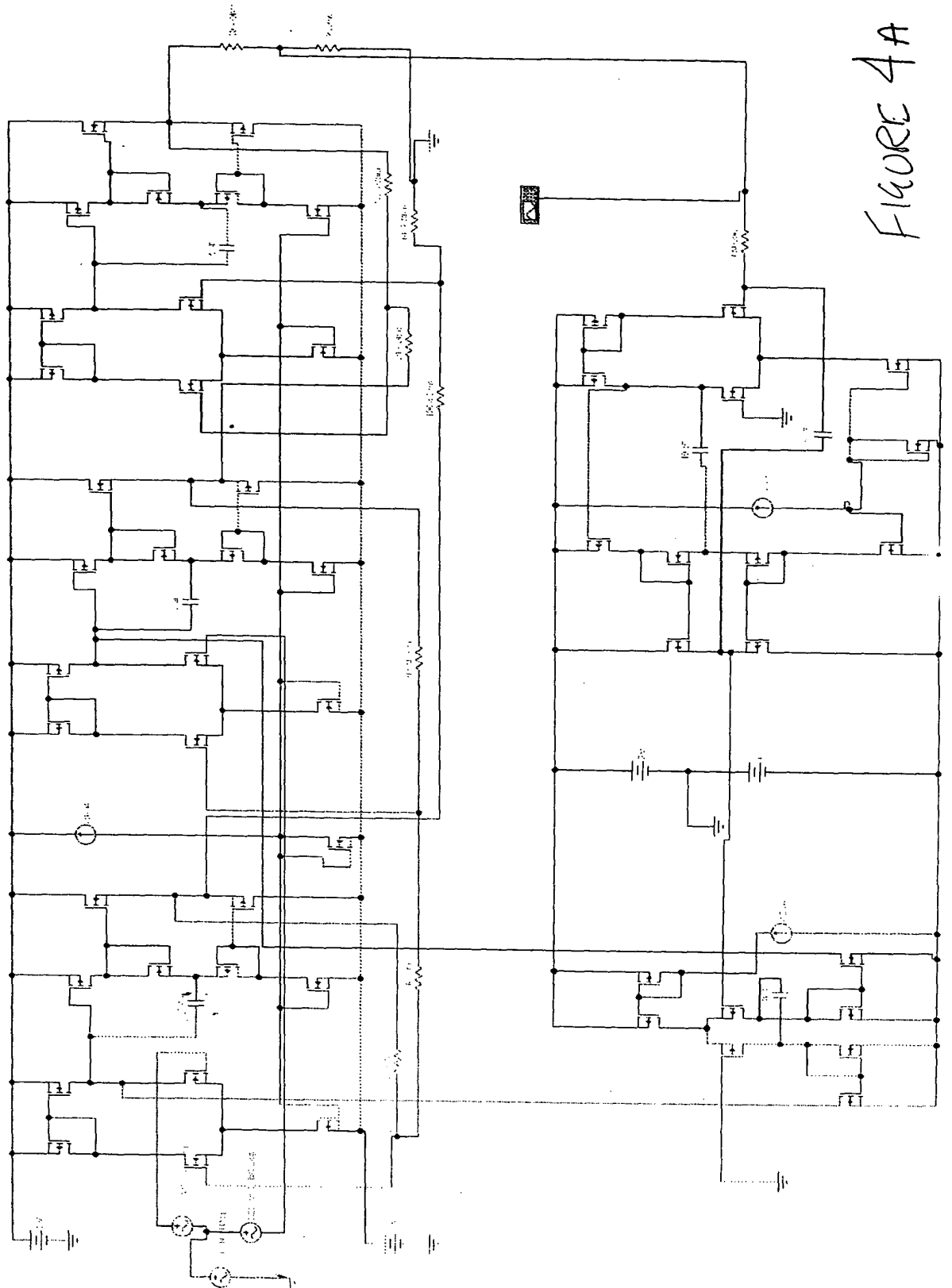
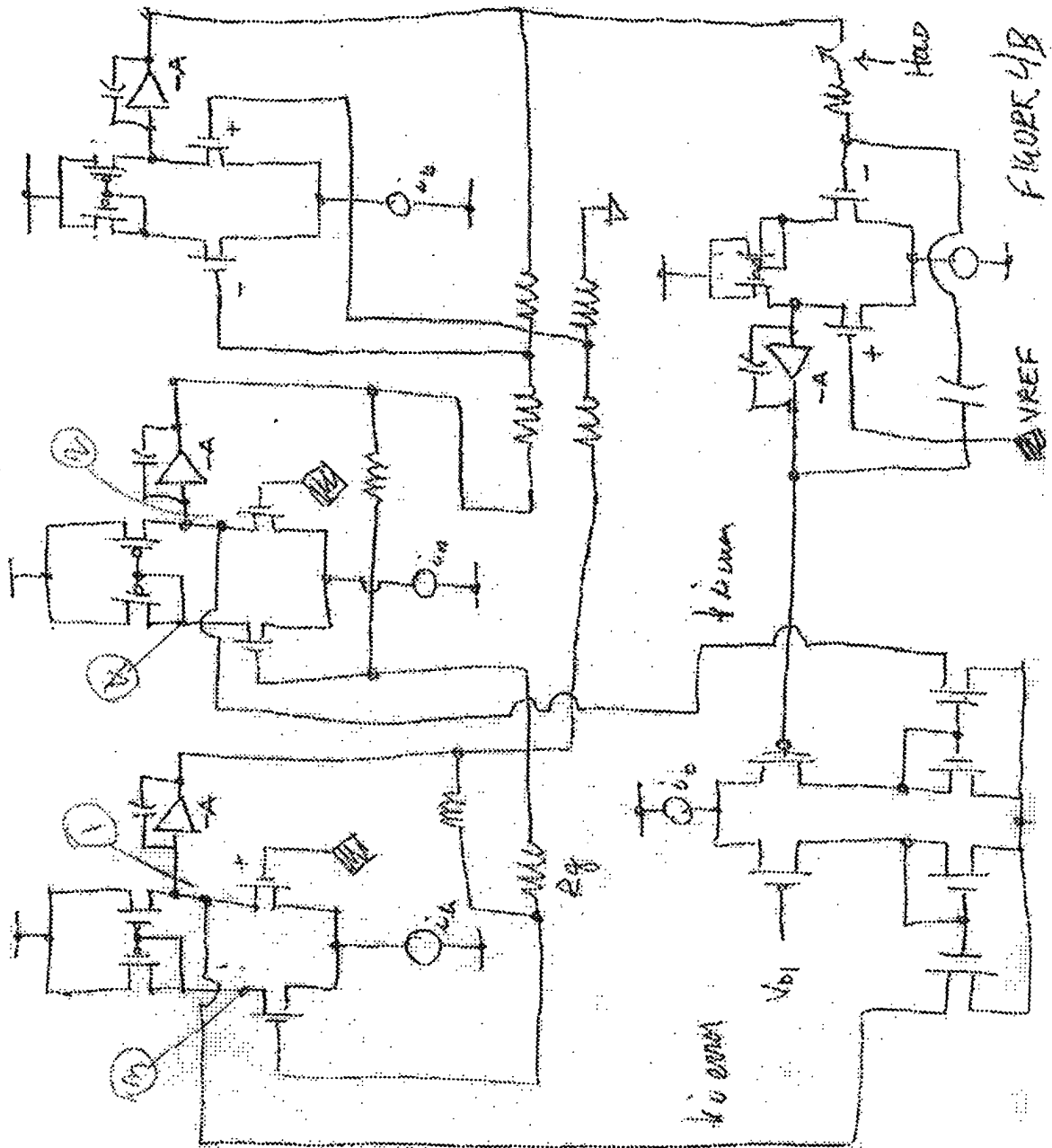


FIGURE 3





\* OPTIONAL BUFFER  
OR GAIN STAGE

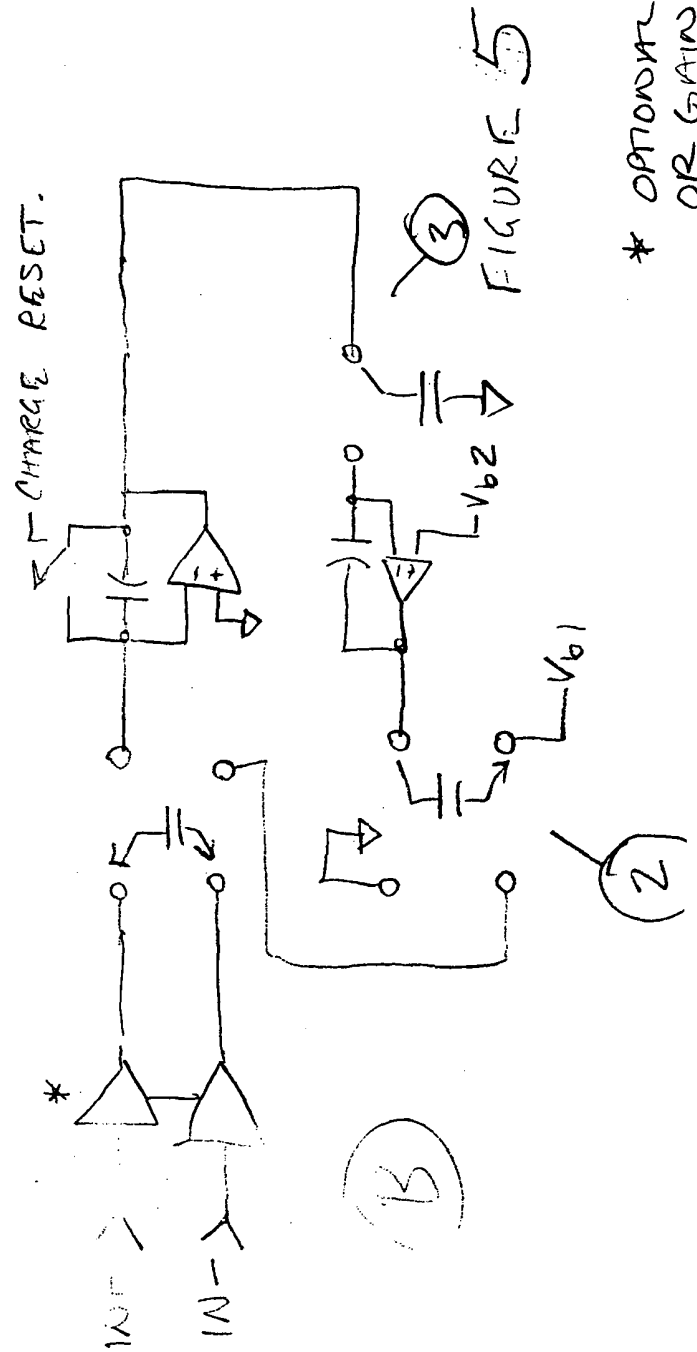
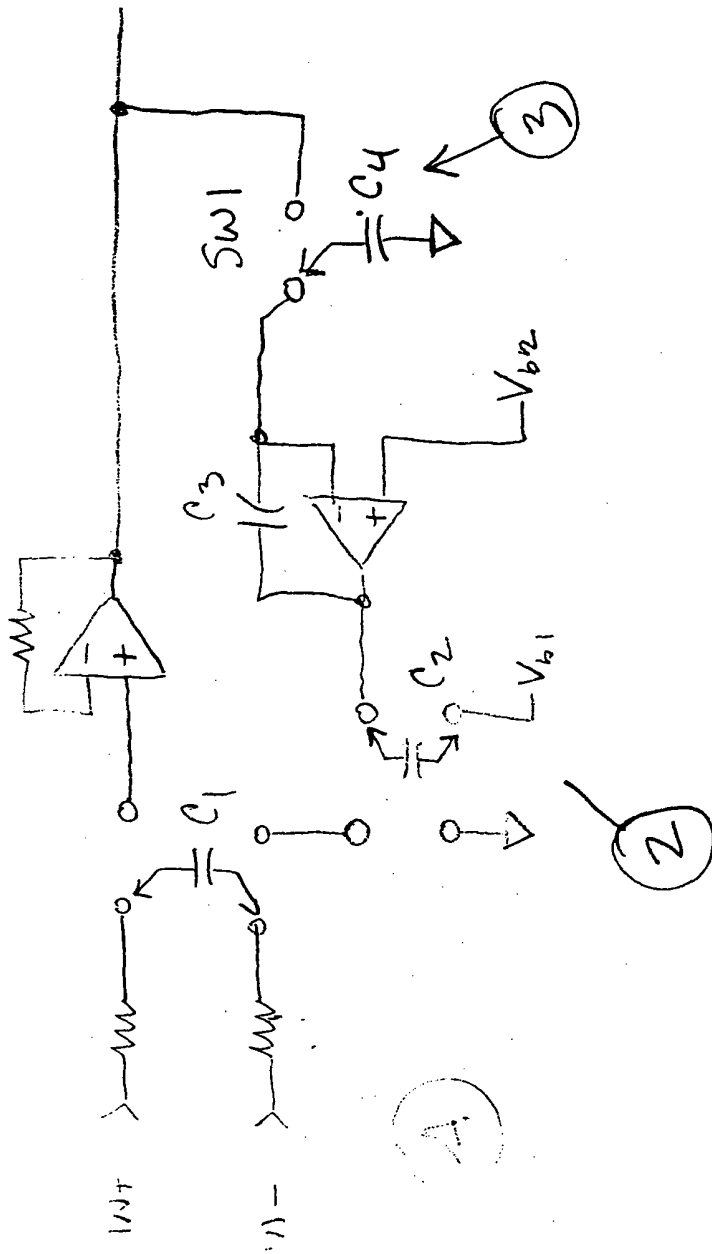
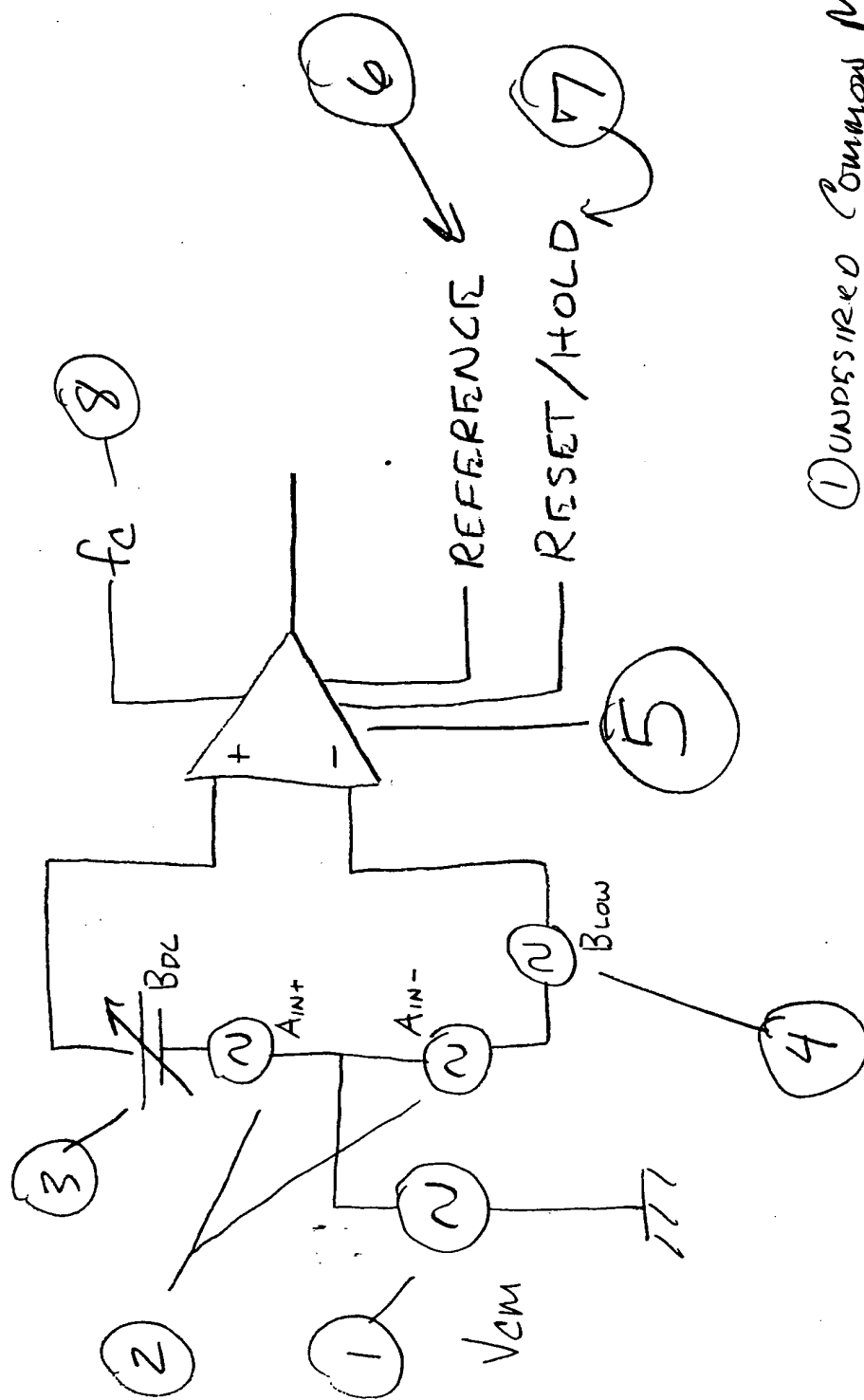


FIGURE 5



- ① UNDESIRABLE COMMON MODE
- ② SIGNAL OF INTEREST
- ③ UNDESIRABLE DC
- ④ UNDESIRABLE LOW FREQUENCIES

FIGURE 6

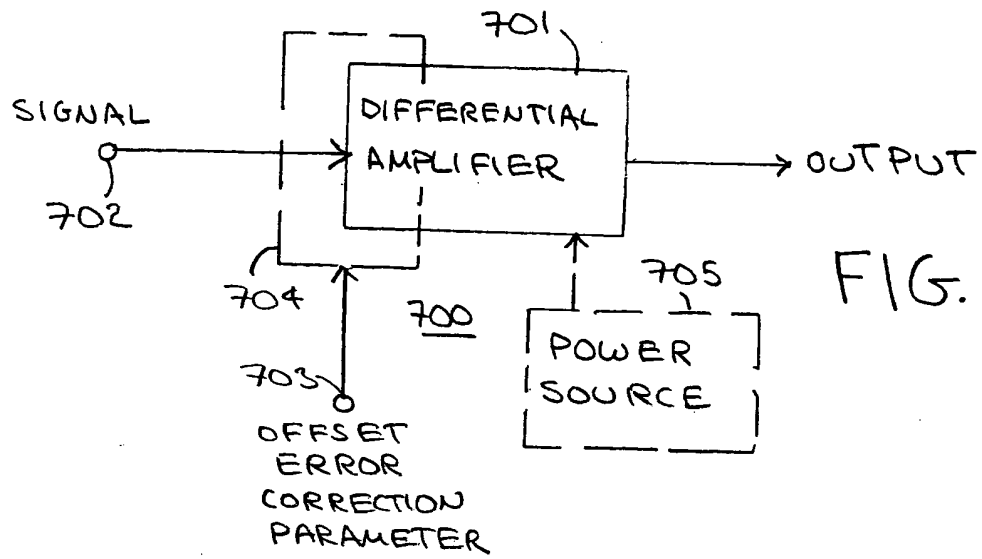


FIG. 7